ANNEXURE "C"

ENQUIRY FORM

DEPARTMENT OF ELECTRONICS ENGINEERING
Z.H. College of Engineering & Technology
ALIGARH MUSLIM UNIVERSITY
ALIGARH

Ref. No.ELC-1/ 12-13/FIST Dated: 16.01.2013

M/s.....

Dear Sir,

Please quote your lowest rates allowing concessions given to Technical Institutions, if any, for the following articles. Illustrations/ specification, stating delivery time, should be given in each case. Quotation under sealed cover should reach this office of the Electronics Engineering Dept. with in 14 days/ latest by 31.01.2013.

S.No.	Approximate quantity needed	Particulars
		A DSO with Logic Analyzer is required for research Lab with following Specification. (i). Higher sampling rate — 5 GS/s (4 Channels), 10 GS/s (2 Channel) (ii). Rise Time — 350 ps (iii). Vertical sensitivity — 2mv/div - 10 V / div (iv). Vertical accuracy (DC Gain Accuracy) — +/- 1 % of Full Scale (v). Time base range — 200 ps/div - 1000 s/div (vi). Time base Accuracy — </= 5 ppm (vii). Memory (Record Length) — 16 M points (4 channel), 32 M points (2 channel) (viii). Digital Record Length — 10 M points (iX). Digital Signal Speed — 250 MHz or better (X). (Operating System) Microsoft Windows XP Professional or better (Xi). I/O interface (Ethernet Port, USB Port, etc) (Xii). Ease of use 2- Arbitrary Function Generator (i) No. of channels — Two (ii) Frequency — 25 MHz or better (iii) Display — Color (iv) Pulse — 12.5 M Hz or better (v) Sampling rate — 250 MS/s or better (vi) Amplitude — 10 V p-p or 20 V p-p (vii) Interface — USB, LAN, GRIB

CHAIRMAN
Electronics Engg. Department
A.M.U., Aligarh