

TENDER INVITATION NOTICE

The tenders are invited for the following items (**Package No. TEQIP-II-1AMU03-62 entitled “Computer Engg Items”**) through “National Competitive Bidding” process of World Bank under TEQIP-II Project.

Item no.	Specifications	Quantity
Design Boards for Altium Front End tools	Design Boards for Altium Front-End tools •Nanoboard support •Range of auto-configured, swappable target FPGA daughter boards (from all chip vendors) are supported plus plug-in peripheral boards for complete flexibility in system architecture, Power Monitor for FPGA designs	1
Altium Front-end tools with perpetual licence	Altium front-end tools with perpetual licence •DXP Platform •Schematic – Viewer?PCB – Viewer?CAM File – Viewer?Schematic – Soft Design Editing Simulation – VHDL •NanoBoard Support •FPGA Design?FPGA Processor Cores •Processor Core Embedded Tools •Programmable FPGA-Based Instruments •Soft Device JTAG Support Hard Device JTAG Support IP Core Design Re-Use Import/Export •Schematic – Editing	1
MITS Eleven PCB Design Kit	MITS Eleven PCB Design Kit •Designing PCB layouts for circuits •Standard Equipped Camera Monitoring System •Single Step Tool Change •Spindle Speed: 41000 rpm •Low-cost, wide range table size, XYZ triple-axes control •High resolution	1

- Interested bidders may purchase the Bid Document for Rs. 500/- (Non-refundable) from the office of the Principal, ZHCET, AMU, Aligarh.
- The date of commencement of sale of bid document is 20.04.2013 time 10:00 hrs.
- The last date for sale of Bidding Document is 20.05.2013 time 16:00 hrs.
- The last date for receipt of bids is 20.05.2013 time 16:00 hrs.
- Rest of the details will be made available in the bid document.
- The Demand Draft of Rs. 500/- should be in favour of “MHRD/NPIU under TEQIP-II (Z.H.College of Engg. & Tech., AMU)”, payable at Aligarh, U.P.
- The bidder should have registration with Central Govt. Agency (as supplier) or must have DGS&D Registration.
- Quoted amount should be in Indian Rupees only.

(Prof. Ekram Husain)
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